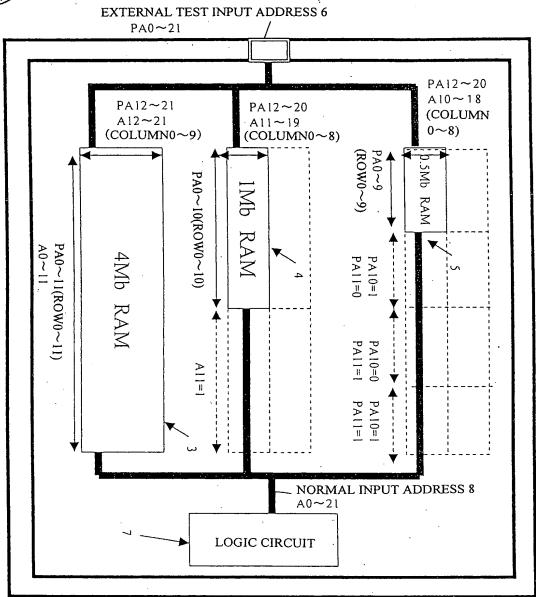


MEMORY EMBEDDED SYSTEM LSI 1







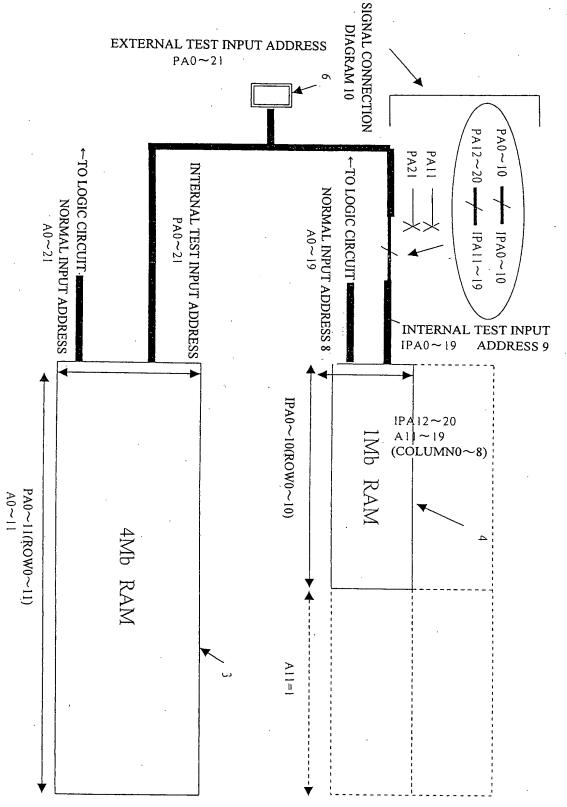
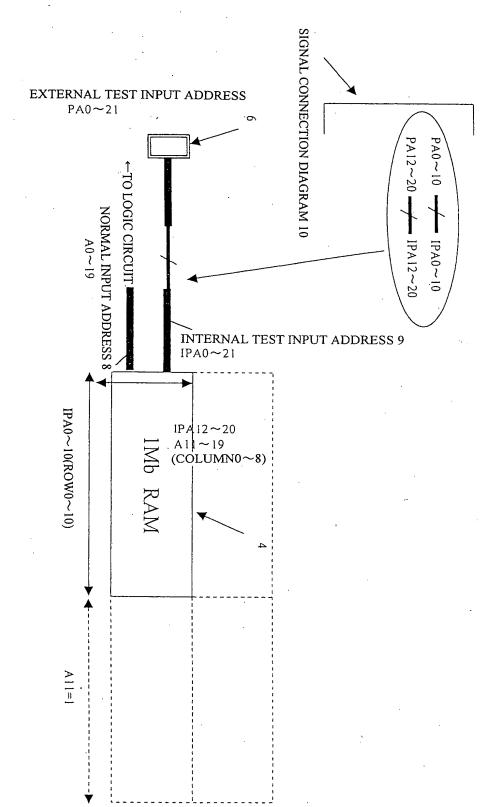
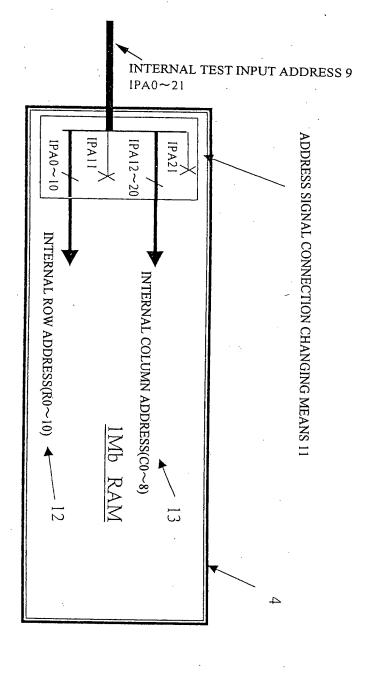


FIG. 3









IG. 5



FIG. 6

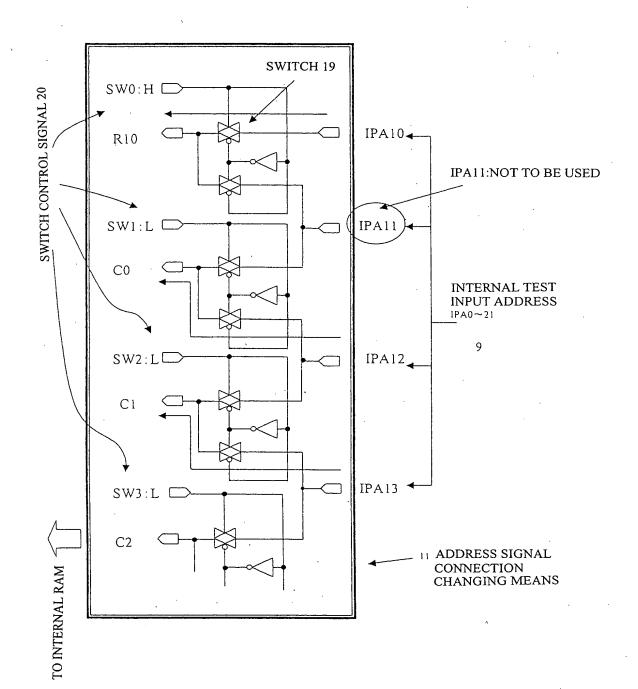
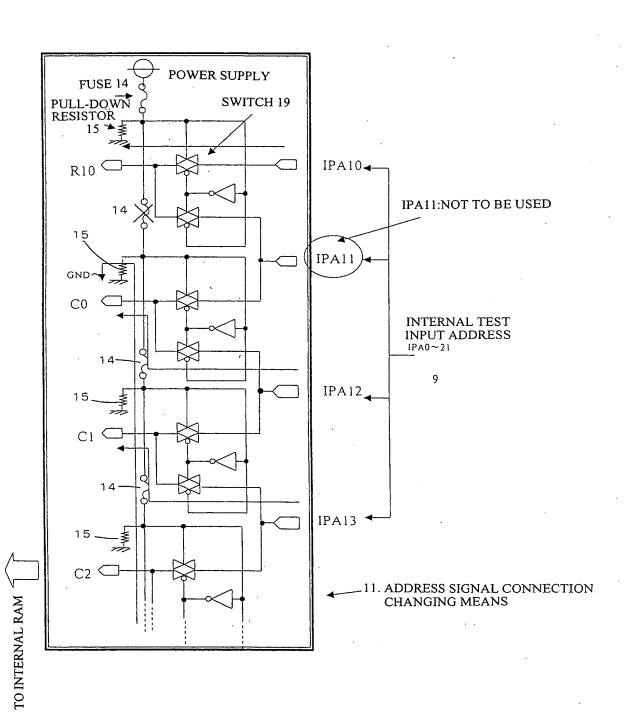




FIG. 7



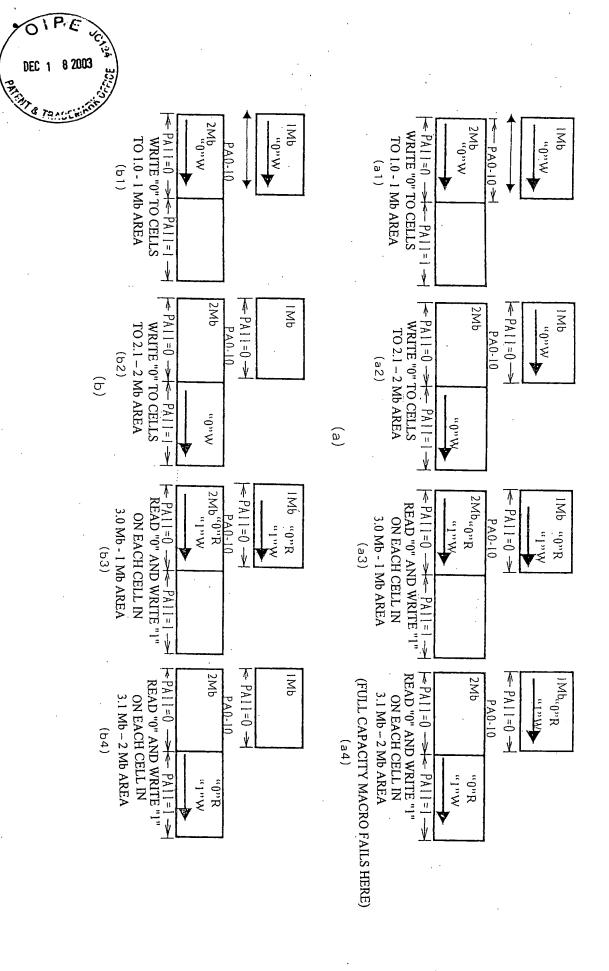
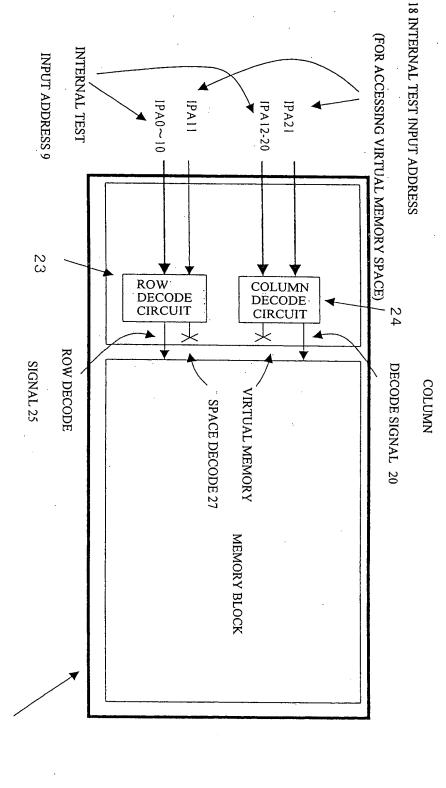


FIG. 9



4. IMb RAM